

L Number	Hits	Search Text	DB	Time stamp
1	258	(kerr-robert shirley-brian tran-luan-c lowrey-tyler-a).in.	USPAT; US-PGPUB; EPO; DERWENT	2003/12/23 08:08
3	32	((kerr-robert shirley-brian tran-luan-c lowrey-tyler-a).in.) and overlap\$4	USPAT; US-PGPUB; EPO; DERWENT	2003/12/23 08:18
4	2229	(plug interconnect\$3 (conducti\$2 adj1 line) bitline opening trench) with overlap\$4 with (gate electrode diffus\$3 drain (active adj1 (region area)) source "D/S")	USPAT	2003/12/23 08:37
5	11598	(opening hole trench recess via) with (overlap\$4 \$3align\$4) with (electrode gate storage).bi.	USPAT	2003/12/23 08:41
6	1377	((opening hole trench recess via) with (overlap\$4 \$3align\$4) with (electrode gate storage).bi.) and (DRAM SRAM)	USPAT	2003/12/23 11:46
7	3	("6057581"   "6100138"   "6117733").PN.	USPAT	2003/12/23 10:14
8	7261	(bitline plug opening trench via hole) with (misalgn\$4 (mis adj1 align\$3) align\$4) with (gate electrode silicide polycide).bi.	USPAT	2003/12/23 11:46
9	1085	((bitline plug opening trench via hole) with (misalgn\$4 (mis adj1 align\$3) align\$4) with (gate electrode silicide polycide).bi.) and (DRAM SRAM)	USPAT	2003/12/23 12:02
10	104	((bitline plug opening trench via hole) with (misalgn\$4 (mis adj1 align\$3) align\$4) with (gate electrode silicide polycide).bi.) and (DRAM SRAM)) not ((opening hole trench recess via) with (overlap\$4 \$3align\$4) with (electrode gate storage).bi.) and (DRAM SRAM))	USPAT	2003/12/23 11:59
11	272	butt adj1 contact.bi.	USPAT	2003/12/23 12:00
12	26	(butt adj1 contact.bi.) and (DRAM SRAM)	USPAT	2003/12/23 12:23
13	1	6380023.pn. and direct\$3	USPAT	2003/12/23 12:24